AMENDMENTS TO THE SPECIFICATION

Please amend the specification at the second and third paragraphs on page 22 and continuing onto the top of page 23 as follows:

--Accordingly, under the control of the CPU 112, the N-FB1 122 and the N-FB2 124 in the frame buffer 114 alternately receive and store the data output by the N-bit unit from the bit counter 106, and the stored data is decoded by the turbo decoder 116. When user data decoded by sub frame unit, the decoded data output from the turbo decoder 116 is recomposed or reassembled into the frames of the original length by a frame reassembler ~~recomposer~~ 118 which is controlled by the CPU 112, and then output as the user data through a source data decoder 120.

In summary, the turbo decoder 116, broadly described, receives a super frame consisting of multiple frames or multiple sub frames segmented from a frame, and turbo decodes the received frames. The frame reassembler ~~recomposer~~ 118, under the control of the CPU 112, recomposes or reassembles, when user data decoded by sub frame unit the output of the turbo decoder 116 into the original frames in response to information about the frame size and number of the frames constituting the sub frames or information about the number of the sub frames segmented from the input frame and the size of the sub frames. The frame reassembler ~~recomposer~~ 118, under control of the CPU 112 segments when user data decoded by super frame unit, the output of the turbo decoder 116 into the original frames in response to information about the frame size and number of the frames constituting super frame.--